

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	102792	ring and modulat\$5	US-PGPUB; USPAT	OR	ON	2007/06/28 13:25
L2	84802	(multiplexer demultiplexer multiplex demultiplex) and @ad<"19991028"	US-PGPUB; USPAT	OR	ON	2007/06/28 13:21
L3	4721	L2 and 1	US-PGPUB; USPAT	OR	ON	2007/06/28 13:21
L4	2756	3 and clock	US-PGPUB; USPAT	OR	ON	2007/06/28 13:21
L5	2499	4 and frequen\$5	US-PGPUB; USPAT	OR	ON	2007/06/28 13:23
L6	638	5 and "370".clas.	US-PGPUB; USPAT	OR	ON	2007/06/28 13:23
L7	4759	ring with modulat\$5	US-PGPUB; USPAT	OR	ON	2007/06/28 13:25
L8	288	7 and 2	US-PGPUB; USPAT	OR	ON	2007/06/28 13:25
L9	0	8 and clook	US-PGPUB; USPAT	OR	ON	2007/06/28 13:25
L10	147	8 and clock	US-PGPUB; USPAT	OR	ON	2007/06/28 13:31
L11	25	10 and (ring with counter)	US-PGPUB; USPAT	OR	ON	2007/06/28 13:29
L12	9	8 and (clock with ring with counter)	US-PGPUB; USPAT	OR	ON	2007/06/28 13:32
L13	7	8 and (clock with (ring adj counter))	US-PGPUB; USPAT	OR	ON	2007/06/28 13:51
L14	0	n?phase?overlap\$4 with clock	US-PGPUB; USPAT	OR	ON	2007/06/28 13:52
L15	4	(n adj phase adj overlap\$4) with clock	US-PGPUB; USPAT	OR	ON	2007/06/28 14:18
L16	4	(n adj phase adj overlap\$4) (multi adj phase adj overlap\$4) with clock	US-PGPUB; USPAT	OR	ON	2007/06/28 14:19
L17	4	((n adj phase adj overlap\$4) (multi adj phase adj overlap\$4)) with clock	US-PGPUB; USPAT	OR	ON	2007/06/28 14:23
L18	13	"614403"	US-PGPUB; USPAT	OR	ON	2007/06/28 14:23
L19	0	"614403".pn.	US-PGPUB; USPAT	OR	ON	2007/06/28 14:23
S1	0	("557564.ap.").PN.	US-PGPUB; USPAT	OR	OFF	2007/05/29 16:37

EAST Search History

S2	0	("6674772.pn.").PN.	US-PGPUB; USPAT	OR	OFF	2007/06/22 08:56
S3	1	"6674772".pn.	US-PGPUB; USPAT	OR	ON	2007/05/29 16:38
S4	1	"20040086002"	US-PGPUB; USPAT	OR	ON	2007/06/22 10:24
S5	84801	(multiplexer demultiplexer multiplex demultiplex) and @ad<"19991028"	US-PGPUB; USPAT	OR	ON	2007/06/28 13:20
S6	560534	(low high lower higher intermediate) with frequency	US-PGPUB; USPAT	OR	ON	2007/06/22 10:31
S7	16436	S5 and S6 and clock	US-PGPUB; USPAT	OR	ON	2007/06/22 10:34
S8	1696	clock with (generate generating gerneration) with bit with rate	US-PGPUB; USPAT	OR	ON	2007/06/22 10:39
S9	285	S5 and S6 and S8	US-PGPUB; USPAT	OR	ON	2007/06/25 15:04
S10	86	S9 and precise	US-PGPUB; USPAT	OR	ON	2007/06/22 11:03
S11	121	S9 and (precise\$3)	US-PGPUB; USPAT	OR	ON	2007/06/22 13:33
S12	59	S11 and ring	US-PGPUB; USPAT	OR	ON	2007/06/22 11:46
S13	56	S12 and phase	US-PGPUB; USPAT	OR	ON	2007/06/22 11:46
S14	130	S9 and (precis\$3)	US-PGPUB; USPAT	OR	ON	2007/06/22 13:33
S15	26	S14 and "370".clas.	US-PGPUB; USPAT	OR	ON	2007/06/25 10:50
S16	1	"20040086002"	US-PGPUB; USPAT	OR	ON	2007/06/25 08:15
S17	84801	(multiplexer demultiplexer multiplex demultiplex) and @ad<"19991028"	US-PGPUB; USPAT	OR	ON	2007/06/25 09:39
S18	560534	(low high lower higher intermediate) with frequency	US-PGPUB; USPAT	OR	ON	2007/06/25 09:39
S19	1696	clock with (generate generating gerneration) with bit with rate	US-PGPUB; USPAT	OR	ON	2007/06/25 09:39
S20	285	S17 and S18 and S19	US-PGPUB; USPAT	OR	ON	2007/06/26 16:01
S21	130	S20 and (precis\$3)	US-PGPUB; USPAT	OR	ON	2007/06/25 09:39
S22	26	S21 and "370".clas.	US-PGPUB; USPAT	OR	ON	2007/06/25 10:50
S23	14	S22 and (demultiplex\$3 de?multiplex\$3)	US-PGPUB; USPAT	OR	ON	2007/06/25 09:40

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S24	2	S22 and (ring adj counter)	US-PGPUB; USPAT	OR	ON	2007/06/25 11:05
S25	0	S22 and (delay adj lock\$2)	US-PGPUB; USPAT	OR	ON	2007/06/25 11:06
S26	20	S22 and (phase adj lock\$2)	US-PGPUB; USPAT	OR	ON	2007/06/25 11:06
S27	9579	clock and ((delay with locked with loop) DLL)	US-PGPUB; USPAT	OR	ON	2007/06/25 11:27
S28	499	S27 and "370".clas.	US-PGPUB; USPAT	OR	ON	2007/06/25 11:28
S29	216	S28 and @ad<"19991028"	US-PGPUB; USPAT	OR	ON	2007/06/25 11:32
S30	7	S29 and (phase with overlap\$4)	US-PGPUB; USPAT	OR	ON	2007/06/25 13:34
S31	3	S20 and DLL	US-PGPUB; USPAT	OR	ON	2007/06/25 15:08
S32	9	S20 and (delay adj lock\$2)	US-PGPUB; USPAT	OR	ON	2007/06/25 15:08
S33	84802	(multiplexer demultiplexer multiplex demultiplex) and @ad<"19991028"	US-PGPUB; USPAT	OR	ON	2007/06/26 08:21
S34	560952	(low high lower higher intermediate) with frequency	US-PGPUB; USPAT	OR	ON	2007/06/26 08:21
S35	1698	clock with (generate generating gerneration) with bit with rate	US-PGPUB; USPAT	OR	ON	2007/06/26 08:21
S36	285	S33 and S34 and S35	US-PGPUB; USPAT	OR	ON	2007/06/26 08:21
S37	130	S36 and (precis\$3)	US-PGPUB; USPAT	OR	ON	2007/06/26 08:21
S38	26	S37 and "370".clas.	US-PGPUB; USPAT	OR	ON	2007/06/26 08:21
S39	6	(US-20040086002-\$).did. or (US-6771613-\$ or US-6310895-\$ or US-6240274-\$ or US-5796796-\$ or US-5787132-\$).did.	US-PGPUB; USPAT	OR	ON	2007/06/26 08:41
S40	6	(one single) and S39	US-PGPUB; USPAT	OR	ON	2007/06/26 08:41
S41	96	S36 and (DLL (delay adj lock\$2) overlap\$4)	US-PGPUB; USPAT	OR	ON	2007/06/26 16:46
S42	14	S41 and "370".clas.	US-PGPUB; USPAT	OR	ON	2007/06/26 16:03
S43	47	S36 and (phase with overlap\$4)	US-PGPUB; USPAT	OR	ON	2007/06/26 16:31
S44	11	S36 and (DLL (delay adj lock\$2))	US-PGPUB; USPAT	OR	ON	2007/06/27 07:43

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S45	22	"6269088"	US-PGPUB; USPAT	OR	ON	2007/06/27 07:43
S46	1	"6269088".pn.	US-PGPUB; USPAT	OR	ON	2007/06/27 07:47
S47	84802	(multiplexer demultiplexer multiplex demultiplex) and @ad<"19991028"	US-PGPUB; USPAT	OR	ON	2007/06/27 07:58
S48	422	S47 and clock and (delay adj lock\$2)	US-PGPUB; USPAT	OR	ON	2007/06/27 07:48
S49	87	S48 and overlap\$4	US-PGPUB; USPAT	OR	ON	2007/06/27 07:49
S50	7	S49 and "370".clas.	US-PGPUB; USPAT	OR	ON	2007/06/27 07:50
S51	1051	S47 and (phase with overlap\$4)	US-PGPUB; USPAT	OR	ON	2007/06/27 07:58
S52	100	S51 and "370".clas.	US-PGPUB; USPAT	OR	ON	2007/06/27 08:00
S53	51	S47 and (phase adj overlap\$4)	US-PGPUB; USPAT	OR	ON	2007/06/27 08:00
S54	6	S53 and "370".clas.	US-PGPUB; USPAT	OR	ON	2007/06/27 08:01
S55	1	S53 and (delay adj lock\$2)	US-PGPUB; USPAT	OR	ON	2007/06/27 08:14
S56	22	"6037812"	US-PGPUB; USPAT	OR	ON	2007/06/27 08:14
S57	1	"6037812".pn.	US-PGPUB; USPAT	OR	ON	2007/06/27 08:17
S58	57	S48 and "370".clas.	US-PGPUB; USPAT	OR	ON	2007/06/27 08:19
S59	197	S47 and (bit with clock) and (delay adj lock\$2 adj loop)	US-PGPUB; USPAT	OR	ON	2007/06/27 08:20
S60	26	S59 and "370".clas.	US-PGPUB; USPAT	OR	ON	2007/06/27 08:20
S61	84802	(multiplexer demultiplexer multiplex demultiplex) and @ad<"19991028"	US-PGPUB; USPAT	OR	ON	2007/06/27 14:26
S62	197	S61 and (bit with clock) and (delay adj lock\$2 adj loop)	US-PGPUB; USPAT	OR	ON	2007/06/27 14:31
S63	26	S62 and "370".clas.	US-PGPUB; USPAT	OR	ON	2007/06/27 14:31